

FIG. 1 is a block diagram of a system architecture.

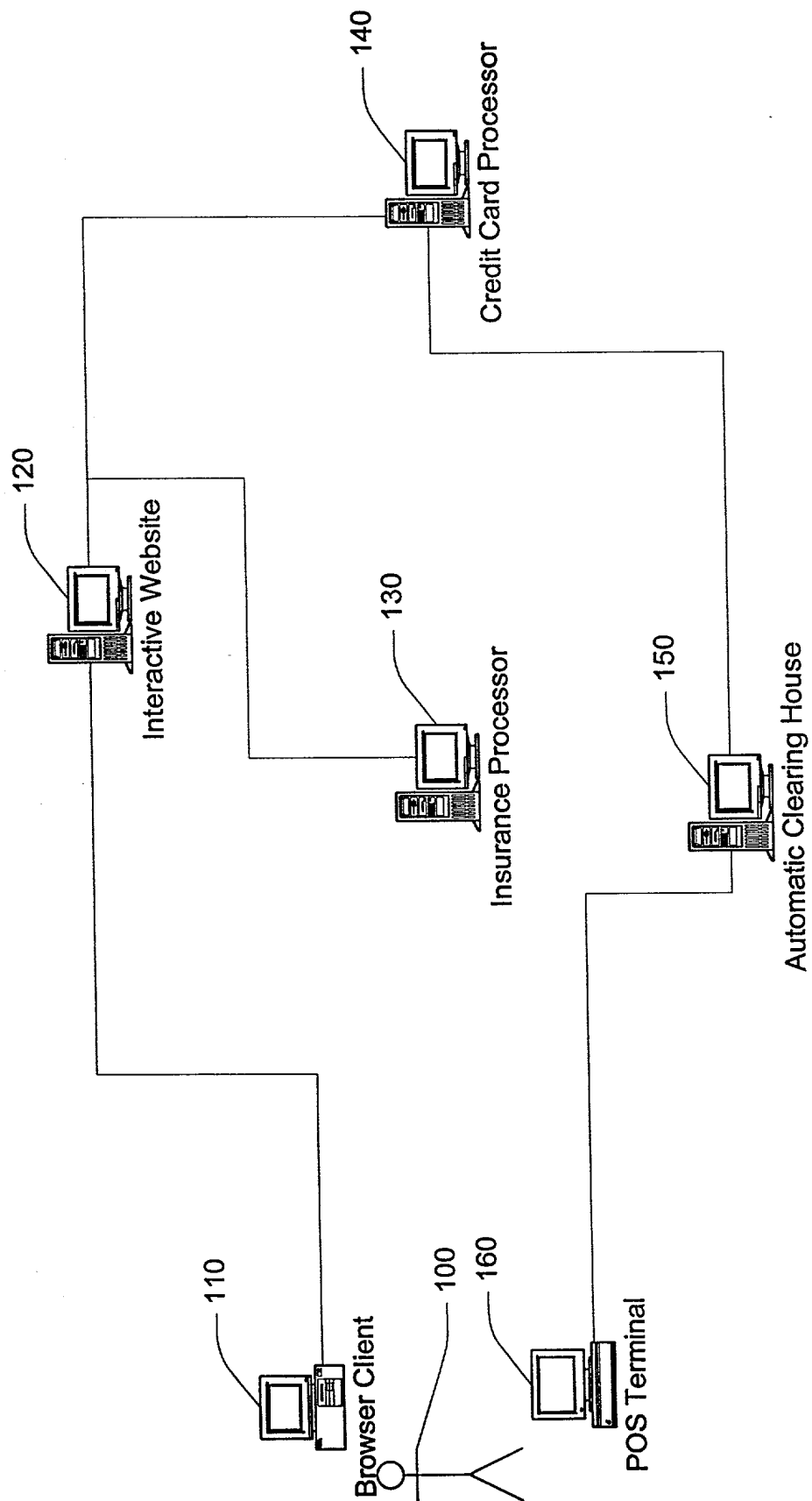


FIG. 1

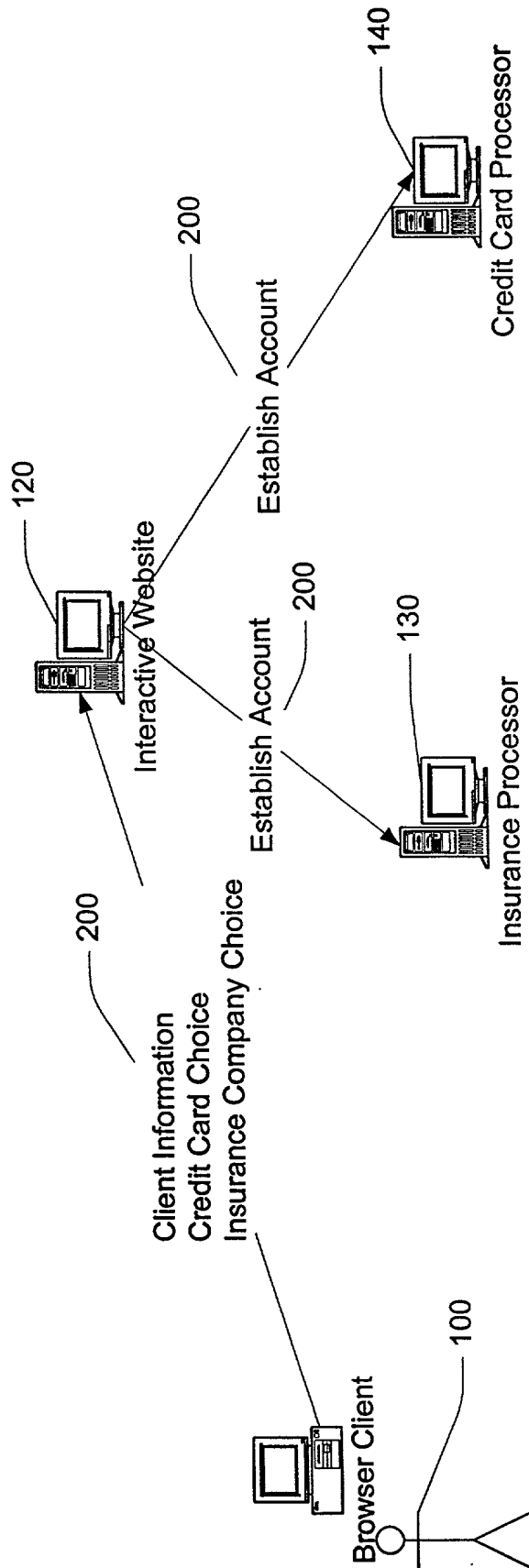


FIG. 2

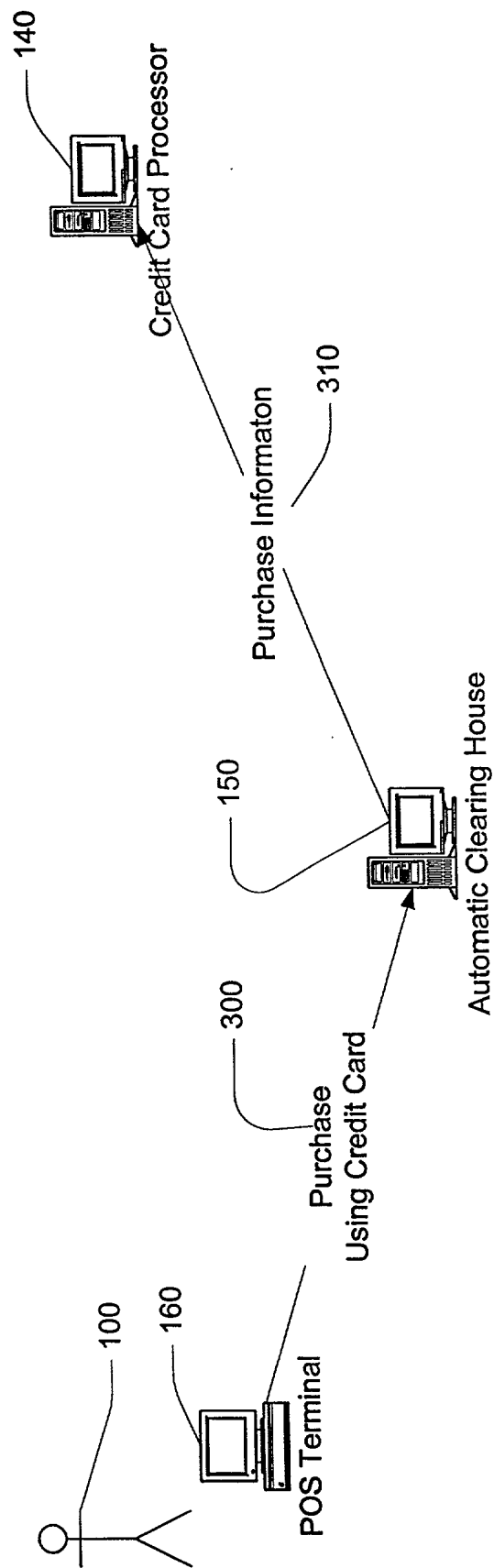


FIG. 3

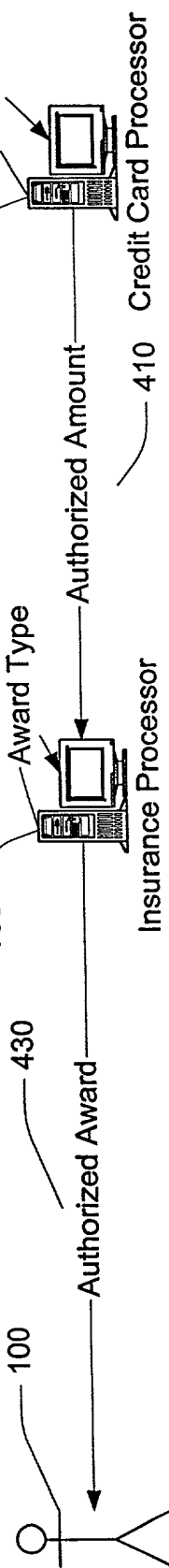


FIG. 4

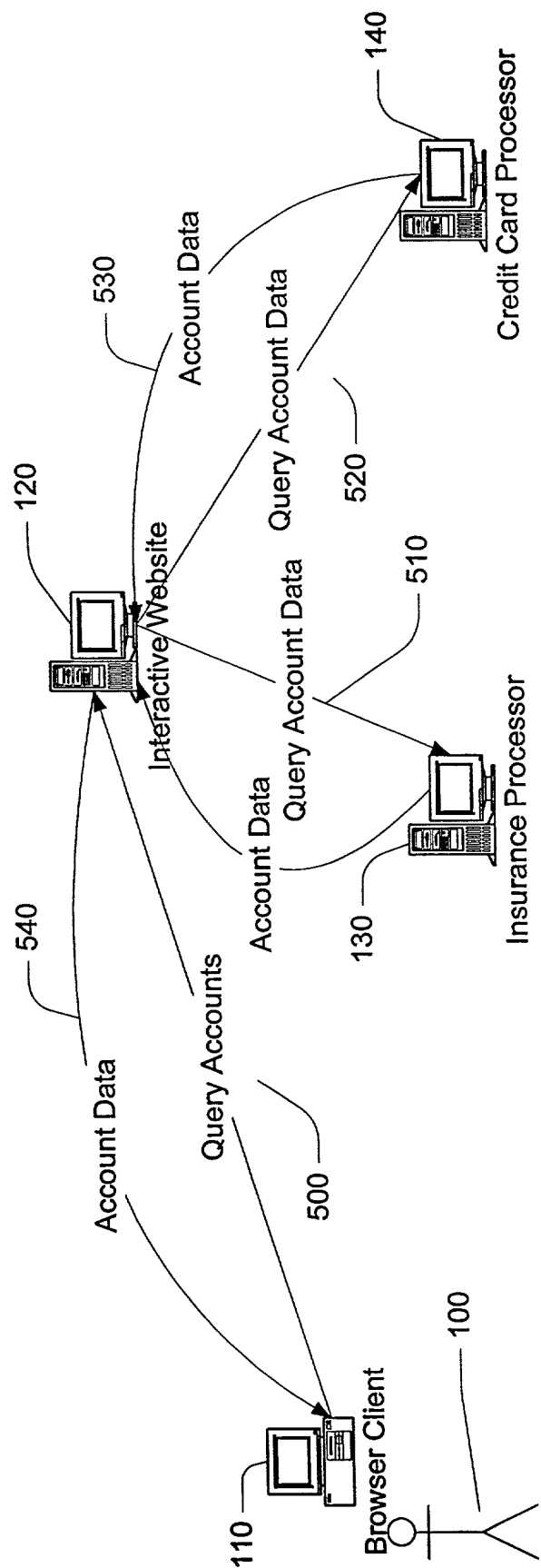


FIG. 5